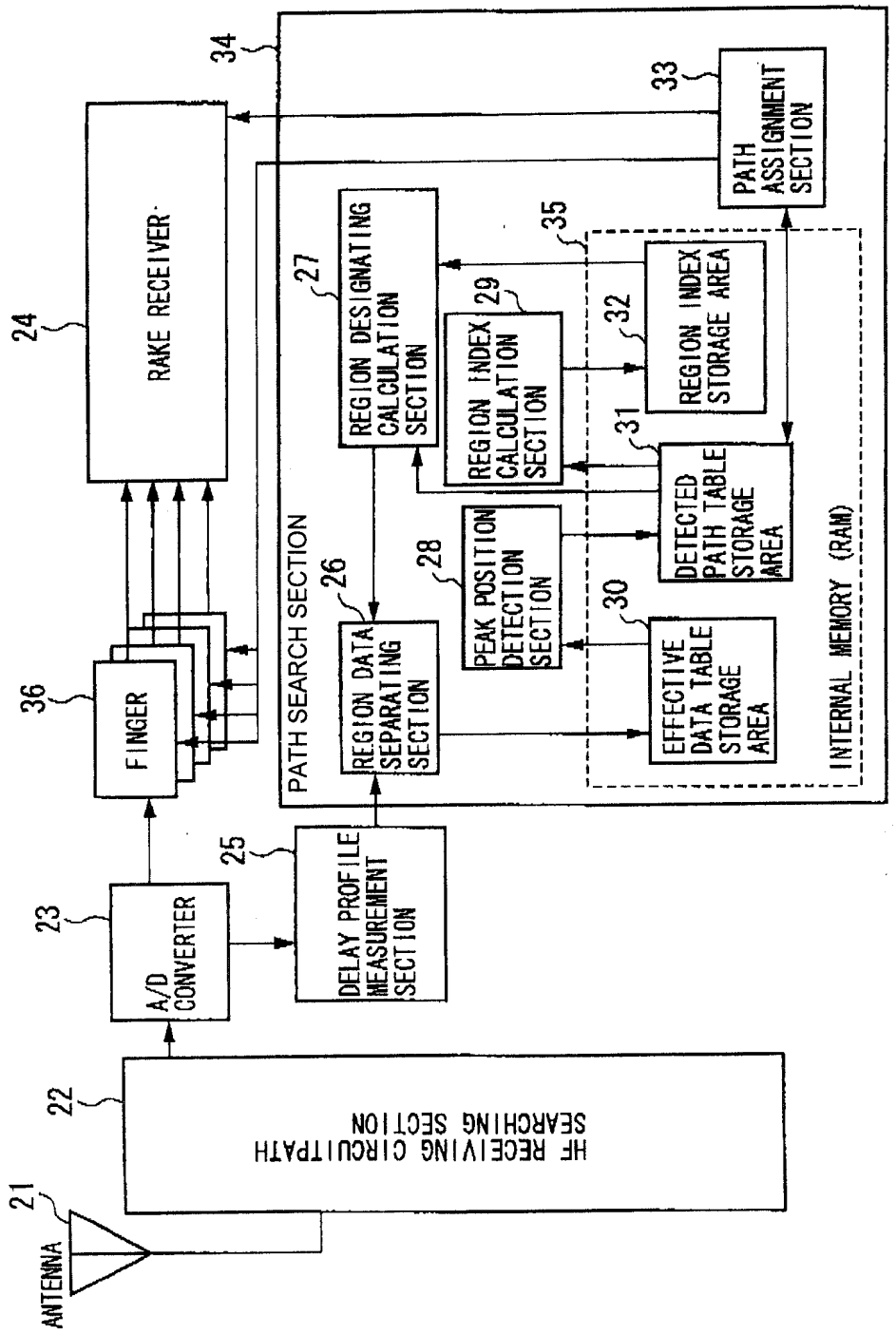
FIG. 1


ANTENNA
21
HF RECEIVING CIRCUIT PATH SEARCHING SECTION
22
A/D CONVERTER
23
DELAY PROFILE MEASUREMENT SECTION
25
FINGER
36
RAKE RECEIVER
24
34
PATH SEARCH SECTION
REGION DESIGNATING CALCULATION SECTION
27
REGION INDEX CALCULATION SECTION
29
PEAK POSITION DETECTION SECTION
28
REGION DATA SEPARATING SECTION
26
INTERNAL MEMORY (RAM)
REGION INDEX STORAGE AREA
32
DETECTED PATH TABLE STORAGE AREA
31
EFFECTIVE DATA TABLE STORAGE AREA
30
35
PATH ASSIGNMENT SECTION
33